STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner:Ji Eun JANG	
Application No./Patent No.: 10/629,672 / 7,107,500	Filed/Issue Date: June 30, 2003 / September 12, 2006
Entitled: TEST MODE CIRCUIT OF SEMICONDUCTOR MEMORY DEVICE	
	rporation of Assignee: corporation, partnership, university, government agency, etc.)
states that it is:	
1. the assignee of the entire right, title, and interest; or	
2. an assignee of less than the entire right, title and inter (The extent (by percentage) of its ownership interest i	
in the patent application/patent identified above by virtue of either:	
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel <u>014352</u> , Frame <u>0644</u> , or a true copy of the original assignment is attached.  OR	
B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:	
1. From:	То :
The document was recorded in the United States I	
Reel, Frame,	
From:  The document was recorded in the United States I	To:
Reel, Frame,	
3. From:	
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Reel, Frame	, or for which a copy thereof is attached.
Additional documents in the chain of title are listed on a supplemental sheet.	
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.  [NOTE: A separate copy ( <i>i.e.</i> , a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]	
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	
/Steve Y. Cho/	09-06-07
Signature	Date
Steve Y. Cho	206-467-9600
Printed or Typed Name	Telephone Number
Attorney of Record, Reg. No. 44,612	
Title	